



8 BIT 烧录脚对应表

| EM78P153E/S | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|-------------|--------|---------|-------------|--|
| | VPP | 7 | 20 | Programming voltage input. |
| | ACLK | 6 | 19 | CLK for OTP memory address increment. |
| | DATAIN | 10 | 23 | ROM code series input and series output pin. |
| | DINCK | 5 | 18 | ROM code series input and output clock. |
| | /PGMB | 8 | 21 | Program write enable.Active low. |
| | /OEB | 9 | 22 | Output enable .Active low. |
| | VDD | 4 | 17 | |
| | VSS | 11 | 24 | |

| EM78P156E/N/EM78P257A | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|-----------------------|--------|---------|-------------|--|
| | VPP | 4 | 15 | Programming voltage input. |
| | ACLK | 15 | 26 | CLK for OTP memory address increment. |
| | DATAIN | 17 | 28 | ROM code series input and series output pin. |
| | DINCK | 18 | 29 | ROM code series input and output clock. |
| | /PGMB | 16 | 27 | Program write enable.Active low. |
| | /OEB | 13 | 24 | Output enable .Active low. |
| | VDD | 14 | 25 | |
| VSS | 5 | 16 | | |

| EM78P447SA/EM78P447NA | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|-----------------------|--------|---------|-------------|--|
| | VPP | 28 | 34 | Programming voltage input. |
| | ACLK | 26 | 32 | CLK for OTP memory address increment. |
| | DATAIN | 24 | 30 | ROM code series input and series output pin. |
| | DINCK | 25 | 31 | ROM code series input and output clock. |
| | /PGMB | 23 | 29 | Program write enable.Active low. |
| | /OEB | 22 | 28 | Output enable .Active low. |
| | VDD | 2 | 8 | |
| VSS | 4 | 10 | | |



| EM78P447SB/EM78P447NB | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|-----------------------|--------|---------|-------------|--|
| | VPP | 30 | 34 | Programming voltage input. |
| | ACLK | 28 | 32 | CLK for OTP memory address increment. |
| | DATAIN | 26 | 30 | ROM code series input and series output pin. |
| | DINCK | 27 | 31 | ROM code series input and output clock. |
| | /PGMB | 25 | 29 | Program write enable.Active low. |
| | /OEB | 24 | 28 | Output enable .Active low. |
| | VDD | 4 | 8 | |
| | VSS | 6 | 10 | |

| EM78P458 | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|----------|--------|---------|-------------|--|
| | VPP | 12 | 22 | Programming voltage input. |
| | ACLK | 10 | 20 | CLK for OTP memory address increment. |
| | DATAIN | 8 | 18 | ROM code series input and series output pin. |
| | DINCK | 9 | 19 | ROM code series input and output clock. |
| | /PGMB | 11 | 21 | Program write enable.Active low. |
| | /OEB | 13 | 23 | Output enable .Active low. |
| | VSS | 5 | 15 | |

| EM78P451 | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|----------|---------|---------|-------------|---------------------------------------|
| | VPP | 2 | 2 | Programming voltage input. |
| | ACLK | 40 | 40 | CLK for OTP memory address increment. |
| | I00-I06 | 27-33 | 27-33 | 7-bit Data Bus pins |
| | C0-C2 | 27-29 | 27-29 | Mode code input pins. |
| | /PGMB | 14 | 14 | Program write enable.Active low. |
| | /OEB | 13 | 13 | Output enable .Active low. |
| | VSS | 1 | 1 | |

| EM78P451S | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|-----------|--------|---------|-------------|----------|
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深圳市瑞丰汇科技有限公司

TEL:0755-83039202 FAX:0755-83038396

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|--------|----|----|---------------------------------------|
| VPP | 2 | 2 | Programming voltage input. |
| ACLK | 40 | 40 | CLK for OTP memory address increment. |
| DATAIN | 27 | 27 | 7-bit Data Bus pins |
| DINCK | 36 | 36 | Mode code input pins. |
| /PGMB | 14 | 14 | Program write enable.Active low. |
| /OEB | 13 | 13 | Output enable .Active low. |
| VDD | 38 | 38 | |
| VSS | 1 | 1 | |

| | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|--------------------------------|--------|---------|-------------|--|
| EM78P468N QFP-64PIN# | VPP | 25 | 2 | 13 Volt for OTP programming |
| | ACLK | 32 | 40 | CLK for address increase |
| | DINCK | 33 | 36 | CLK for DATAIN pin |
| | DATAIN | 34 | 27 | ROM series Input/Output pin |
| | /PGMB | 38 | 14 | Program enable control pin , active low |
| | /OEB | 39 | 13 | Output enable control for DATAIN,active low for output |
| | VDD | 29 | 38 | |
| | GND | 26 | 1 | |

| | SYMBOL | PIN NO. | DWTR SOCKET | FUNCTION |
|----------------------------------|--------|---------|-------------|--|
| EM78P468N L/QFP-44PIN# | VPP | 14 | 2 | 13 Volt for OTP programming |
| | ACLK | 21 | 40 | CLK for address increase |
| | DINCK | 22 | 36 | CLK for DATAIN pin |
| | DATAIN | 23 | 27 | ROM series Input/Output pin |
| | /PGMB | 25 | 14 | Program enable control pin , active low |
| | /OEB | 26 | 13 | Output enable control for DATAIN,active low for output |
| | VDD | 18 | 38 | |
| | GND | 15 | 1 | |